


KANNUR UNIVERSITY
(EXAMINATION BRANCH)

No.EGI.2/B Tech-TT/10

Thavakkara, Civil Station P O,
Dated: 13.06.2017

Ref No:EG 1.2/B.Tech.EX/2008 dtd-23.05.17

TIMETABLE FOR IVth SEMESTER B Tech DEGREE
(SUPPLEMENTARY/IMPROVEMENT –
INCLUDING PART TIME) EXAMINATIONS, MAY 2017

Time : 9.30 AM to 12.30 PM

2007 ADMISSION ONWARDS

Day & Date	Paper	Subject
28.06.2017 WEDNESDAY	PT2K6/2K6 CE/ME/EE/EC/ CS/IT/AEI 401	Engineering Mathematics III
30.06.2017 FRIDAY	PT2K6/2K6 CE/ME 402	Humanities
	PT2K6/2K6 EE/EC/AEI 402	Computer Programming
	2K6 CS/IT 402	Data Structures & Algorithms
03.07.2017 MONDAY	PT 2K6/ 2K6 CE 403	Structural Analysis I
	PT 2K6/ 2K6 ME 403	Thermodynamics
	PT2K6/ 2K6 EE 403	Microprocessors & Micro Controllers
	PT2K6/ 2K6 EC 403	Communication Engineering I
	2K6 CS/ IT 403	Systems Programming
	2K6 AEI 403	Electrical Measurements & Measuring Instruments
05.07.2017 WEDNESDAY	PT 2K6/ 2K6CE 404	Fluid Mechanics II
	PT 2K6/ 2K6 ME 404	Manufacturing Processes
	PT 2K6/ 2K6 EE 404	Pulse & Digital Electronics
	PT 2K6/2K6 EC/AEI 404	Signals & Systems
	2K6 CS/IT 404	Microprocessors and Microcontrollers

07.07.2017 FRIDAY	PT 2K6/2K6 CE 405	Surveying II
	PT 2K6/2K6 ME 405	Fluid Machinery
	PT 2K6/2K6 EE 405	Electrical Machines I
	PT 2K6/2K6 EC/ AEI 405	Electronic Circuits
	2K6 CS 405	Computer Organization & Design
	2K6 IT 405	Discrete Computational Structures
10.07.2017 MONDAY	PT 2K6/2K6 CE 406	Engineering Geology
	PT 2K6/2K6 ME 406	Machine Drawing
	PT 2K6/2K6 EE 406	Electrical Engineering Materials
	PT 2K6/2K6 EC 406	Digital Electronics
	2K6 CS 406	Electric Circuits & Systems
	2K6 IT 406	Programming Language Concepts
	2K6 AEI 406	Digital Electronics

Sd-

JOINT REGISTRAR (EXAMS)
FOR CONTROLLER OF EXAMINATIONS